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July 29, 2003

ATTORNEY DOCKET NO. 04329.3105
CUSTOMER NO. 22,852



Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

New U.S. Patent Application
Title: SEMICONDUCTOR DEVICE MANUFACTURING METHOD

Inventors and Addresses:

Tsutomu TEZUKA
Yokohama-shi, Japan

Shinichi TAKAGI
Tokyo, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 32 pages, including 3 independent claims and 20 claims total.
2. Drawings- 5 sheets of drawings containing 19 figures.
3. Certified copy of Japanese Patent Application No. 2002-220030, filed July 29, 2002.
4. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with document attached.
5. The filing fee is calculated as follows:

Basic Application Filing Fee					\$750	\$ \$750.00
	Number of Claims		Basic	Extra Claims		
Total Claims	20	-	20		x \$18	
Independent Claims	3	-	3		x \$84	
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$280	
Subtotal						\$ 750.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 750.00

A check for \$750.00 is enclosed.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicants claim the right to priority based on Japanese Patent Application No. 2002-220030, filed July 29, 2002.

Please address all correspondence with respect to this application to:

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Washington, D.C. 20005-3315

Please accord this application an application number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this

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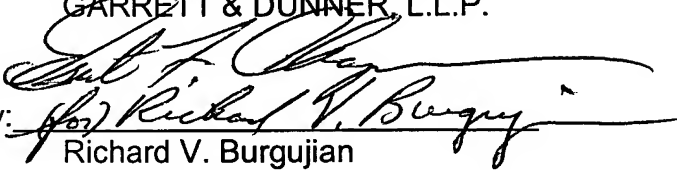
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application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By:


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RVB/FPD/dvz
Enclosures

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